

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,962,147 B2
APPLICATION NO. : 10/003500
DATED : November 8, 2005
INVENTOR(S) : Ernest J. Hamilton

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page:

Directly below section (76), skip a line and insert --(73) Assignee: **Micron Technology, Inc.**, Boise, Id. --

In the drawings:

In FIG. 8,

change the right-hand occurrence of element number "114" to --105--

In the specification:

COLUMN 2, LINE 37,	change "Kievoneit" to --Klievoneit--
COLUMN 6, LINES 2-3	change "threaded element 114" to --tapered surface 105--
COLUMN 6, LINE 5,	change "threaded element 114" to --tapered surface 105--
COLUMN 6, LINE 8,	change "threaded element 114" to --tapered surface 105--
COLUMN 9, LINE 50,	change "4" to --4A--

In the claims:

CLAIM 17, COLUMN 12, LINE 15,	change "of each of shaped flange" to --of each shaped flange--
CLAIM 29, COLUMN 12, LINE 49,	change "claims 17" to --claim 17--

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,962,147 B2
APPLICATION NO. : 10/003500
DATED : November 8, 2005
INVENTOR(S) : Ernest J. Hamilton

Page 2 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Please replace FIG. 8 with the following:

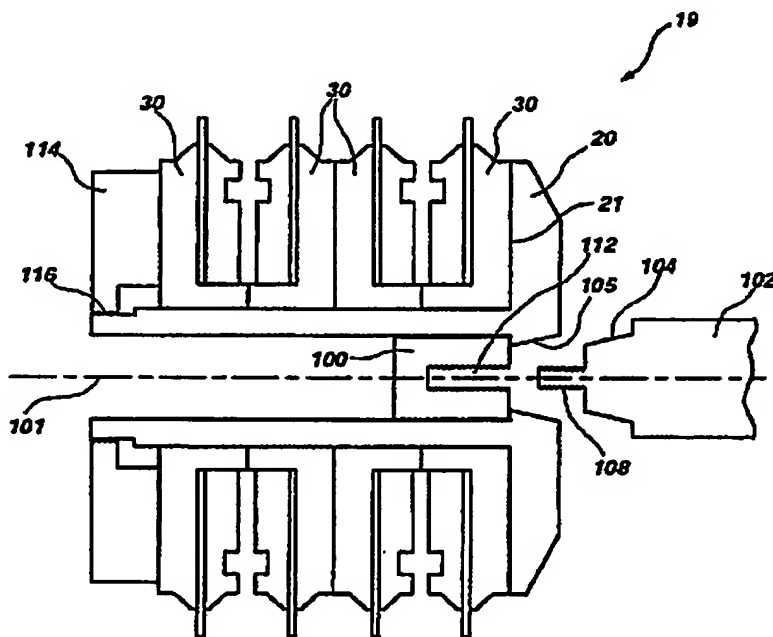


Fig. 8

Signed and Sealed this

Sixth Day of March, 2007

JON W. DUDAS
Director of the United States Patent and Trademark Office